

Sheet \_\_\_\_\_ of \_\_\_\_\_

<b>Form PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>	<b>Docket Number (Optional)</b> <b>A5GN2375US</b>	<b>Application Number</b>  <b>Applicant</b> <b>Bucklen, Willard Kraig</b>
<b>Filing Date</b>		<b>Group Art Unit</b>

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DC	5 7 9 6 3 9 2	8/18/98	Eglit	345	213	
DC	6 0 4 9 3 6 0	4/11/00	Yanai, et al.	348	584	
DC	6 1 5 7 3 7 6	12/5/00	Eglit	345	213	
DC	6 2 3 2 9 5 2	5/15/01	Eglit	345	134	
DC	6 3 2 0 5 7 4	11/20/01	Eglit	345	213	
DC	6 4 5 9 4 2 6	10/1/02	Eglit	345	213	

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

DC	Diniz, George, et al, "Bringing Displays into the Digital Future", EDN Magazine, April 26, 2001, pp. 105-114
DC	Kim, Beomsup, et al., "A 30-MHz Hybrid Analog/Digital Clock Recovery Circuit in 2-um CMOS", IEEE Journal of Solid-State Circuits, Vol. 25, No. 6, December, 1990, pp. 1385-1394

<b>EXAMINER</b> <div style="text-align: center;">/Doon Chow/</div>	<b>DATE CONSIDERED</b> <div style="text-align: center;">06/10/2006</div>
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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP§609; Draw line through